

#### REMARKS

Applicants respectfully request favorable reconsideration of this application, as amended.

The title has been amended to address the objection. Various claims have been amended to address alleged informalities and otherwise to improve the language.

Claims 2-4 and 16-20 have been canceled without prejudice or disclaimer. Thus, Claims 1, and 5-15 are pending for further consideration.

Without acceding to the outstanding rejections under 35 U.S.C. §§ 102(e) and 103(a), Claim 1 has been amended more clearly to recite certain distinctive features of Applicants' invention. In particular, Claim 1 recites that when an address specified by an addressing field coincides with a logical address mapped to the internal memory, the address is set as one of a transfer source address or a transfer destination address of a block transfer. Claim 1 also recites that a complementary transfer source address or transfer destination addresses of the block transfer is a physical address corresponding to a different logical address, and is used as a second transfer object.

Nagayasu teaches a data buffer including an address decoder that writes a transfer destination address into an initial address register if an address on an address bus

designates the initial address register and into an input data register if the address designates the input data register. Nagayasu's data buffer uses address register designations on the address bus to determine where to write a transfer destination address.

Nagayasu does not teach or suggest setting a transfer source address or a transfer destination address of a block transfer to an address when that address coincides with a logical address mapped to the internal memory. Nor does Nagayasu teach or suggest a complementary transfer source address or transfer destination address of the block transfer that is a physical address corresponding to a different logical address.

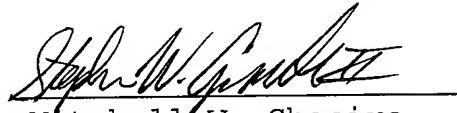
Accordingly, Claim 1 and its dependents distinguish patentably from Nagayasu and should be allowed.

Applicants respectfully request an early Notice of Allowance.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10260) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing

of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

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